The MOSIS Chip Fabrication System

Address:
The Mosis Service
USC/Information Sciences Institute
4676 Admiralty Way
Marina del Rey, CA 90292

E-mail: mosis@mosis.edu

Phone: <none>

Description

MOSIS is system that supports the design and fabrication of VLSI chips via electronic mail. Users e-mail chip specifications to the MOSIS system and MOSIS will do basic checks on the specification and then arrange for it to be fabricated. The resulting chips are shipped (via U.S. mail, to the requestor).

MOSIS also handles user inquiries via e-mail. To request basic information send the following message:

```
REQUEST: INFORMATION
TOPIC: TOPICS
REQUEST: END
```

To get a copy of the User Manual, send the following message:

```
REQUEST: INFORMATION
TOPIC: USER_MANUAL
REQUEST: END
```

To reach a member of the MOSIS staff, send the following note:

```
REQUEST: ATTENTION
(your message here)
REQUEST: END
```

Take care not to include any colons (':'s) in message, as MOSIS will try to parse it and your message may be garbled as a result.

Network access

MOSIS can be reached via e-mail at mosis@mosis.edu.

Who Can Use MOSIS

MOSIS charges for its services. To use MOSIS you must either provide a purchase order to cover your costs, or be sponsored by DARPA or NSF. A program to provide support for university classes in VLSI design may also provide funding. For detailed information, contact the MOSIS project.

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